Date: March 28, 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Alexandria, VA 22313-1450.

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450,

Patent Application

Applicant(s): J.M. Brennan et al.

Case:

2-81-1-4

Serial No.:

10/773,614

Filing Date:

February 6, 2004

Group:

2823

Examiner:

Brook Kebede

Title:

Semiconductor Device Configured for

Reducing Post-Fabrication Damage

AMENDMENT TRANSMITTAL

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is the following document relating to the above-identified patent application:

(1) Amendment After Final Rejection Under 37 C.F.R. §1.116.

There is no additional fee due in conjunction with the Amendment. In the event of any nonpayment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762 as required to correct the error.

Respectfully submitted,

Date: March 28, 2006

Wayne L. Ellenbogen

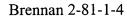
Reg. No. 43,602 Attorney for Applicant(s)

Ryan, Mason & Lewis, LLP

90 Forest Avenue

Locust Valley, NY 11560

(516) 759-7662



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AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated December 28, 2005, Applicants submit herewith the following Amendment for entry in the above-identified application: